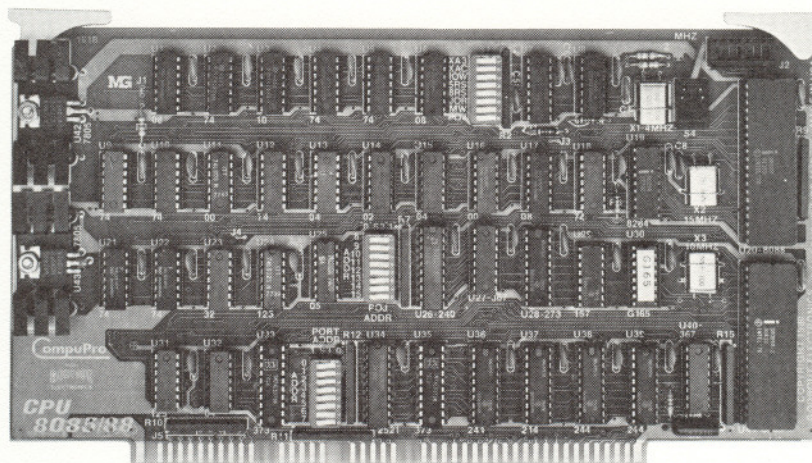


# CPU 8085/88



To achieve an unprecedented level of performance in a CPU board, **CPU 8085/88** includes two processors that exchange tasks to best utilize existing system capabilities. One CPU, the 8088, is an 8 bit bus version of the 8086 16 bit CPU; it has full 16 bit internal architecture, but interfaces with memory and I/O over an 8 bit bus. This approach ensures compatibility with present day machines while providing the speed and power of a true 16 bit computer. The second CPU (an 8085) is a sophisticated 8 bit processor that can run existing software such as CP/M®, and may optionally run at 2 MHz for compatibility with timing dependent software.

One look at the features listed below will show you why the **CPU 8085/88** board excels in high performance industrial, scientific and commercial applications:

- 8085 CPU is downward compatible with the vast library of 8080 software; 8088 CPU is upward compatible with 8086/8088 software, Intel's coming P-series, and other hardware and software not yet developed.
- Provides true 16 bit power with a standard 8 bit S-100 bus.
- Accesses 16 megabytes of memory.
- Fully conforms to all IEEE 696/S-100 bus specifications.
- Switches between CPUs upon receipt of a single input instruction; on-board hardware handles all pertinent switching (DMA can even occur during CPU changeover with no glitches).
- Runs both 8085 and 8088 code in existing S-100 mainframes.
- High speed operation: Both CPUs run at 6 MHz, giving a 200% improvement in throughput compared to 2 MHz systems.
- Designed to accept clock speeds up to 8 MHz, thereby preventing obsolescence when higher speed processors become available.
- Ideal for multi-user installations.

**CPU 8085/88** bridges the 8 and 16 bit worlds to give you the advantages of both modes of operation - without any of the drawbacks. For 8/16 bit software development, advanced computing systems, or multi-user setups, **CPU 8085/88** is an efficient and cost-effective gateway to the future of computing.

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**GODBOUNT**  
ELECTRONICS

# CPU 8085/88

## SPECIFICATIONS

Timing.....	Meets all IEEE specifications*.
Clock Rate (8085).....	2 or 6 MHz, switch selectable.
Clock Rate (8088).....	6 MHz standard, 8 MHz CSC**.
Address Bits.....	24 bits; conforms to IEEE 696/S-100 24 bit extended addressing (16 megabyte) specifications.
Memory Manager (implements extended addressing)/CPU Swap	
Port Address.....	Address selectable by DIP switch.
Data Bus.....	8 bits.
Power-On-Jump.....	DIP switch selectable to any 256 byte boundary.
Jump-On-Reset capability.....	Switch selectable.
I/O Wait States.....	One wait state (switch selectable option).
MWRITE Generator.....	On-board, may be switch disabled.
Front Panel Compatibility.....	Provided.
Method of CPU Swapping.....	Input instruction to swap port.
Swap Time.....	4 clock cycles maximum.
CLOCK (pin 49).....	Always 2 MHz.
Reset, Slaveclr.....	Generated at power-on.
User Manual.....	Comprehensive, with complete circuit description, theory of operation, and schematic.
Speed Upgrade.....	Simply change crystals when faster CPUs become available.
PC Board.....	High quality epoxy, solder masked both sides, component legend, plated through holes, gold plated edge connector fingers.
Sockets.....	Provided for all ICs.
Power Consumption.....	950 mA typical, 1.5A maximum.

\*Except 8088 clock, which exceeds the duty cycle requirements.

\*\*6 MHz 8088 may exceed component height limits of standard size S-100 boards due to heat sinking requirements.

Please consult your local **CompuPro** dealer or the factory for current pricing and delivery information.